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Notice of Allowability	Application No.	Applicant(s)	•
	10/658,720	KATO, TOSHIKAZU	
	Examiner	Art Unit	
	Andy Huynh	2818	
The MAILING DATE of this communication ap All claims being allowable, PROSECUTION ON THE MERITS herewith (or previously mailed), a Notice of Allowance (PTOL-8 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT of the Office or upon petition by the applicant. See 37 CFR 1.3	IS (OR REMAINS) CLOSED in (5) or other appropriate commu RIGHTS. This application is s	this application. If not included inication will be mailed in due cour	se. THIS
1. $igtimes$ This communication is responsive to $the Amendment date 1$	ited 03/24/2005.		
2. \boxtimes The allowed claim(s) is/are <u>1-14</u> .			
3. $igotimes$ The drawings filed on <u>26 November 2004</u> are accepted $igotimes$	by the Examiner.		
4. Acknowledgment is made of a claim for foreign priority a) All b) Some* c) None of the: 1. Certified copies of the priority documents hat 2. Certified copies of the priority documents hat 3. Copies of the certified copies of the priority of International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE noted below. Failure to timely comply will result in ABANDON THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	ave been received. ave been received in Application documents have been received E" of this communication to file	n No I in this national stage application	
5. A SUBSTITUTE OATH OR DECLARATION must be sub INFORMAL PATENT APPLICATION (PTO-152) which g			CE OF
 CORRECTED DRAWINGS (as "replacement sheets") m (a) including changes required by the Notice of Draftsper 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examined Paper No./Mail Date Identifying indicial such as the application number (see 37 CFF each sheet. Replacement sheet(s) should be labeled as such in DEPOSIT OF and/or INFORMATION about the department attached Examiner's comment regarding REQUIREMENT 	erson's Patent Drawing Review er's Amendment / Comment or R 1.84(c)) should be written on the n the header according to 37 CF posit of BIOLOGICAL MATE	in the Office action of te drawings in the front (not the back R 1.121(d). ERIAL must be submitted. Note	
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SI Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposition of Biological Material	B) 6. ☐ Interview St Paper No./ Paper No./ 7. ☒ Examiner's 8. ☒ Examiner's 9. ☐ Other	formal Patent Application (PTO-15 Jummary (PTO-413), Mail Date Amendment/Comment Statement of Reasons for Allowan Charles Examine.	

In the Amendment dated March 24, 2005, claims 15-20 have been canceled, and claims 5

and 6 are amended. Accordingly, claims 1-14 are pending in this application.

Examiner's Amendment

An examiner's amendment to the record appears below. Should the changes and/or

additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR

1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the

payment of the issue fee.

The application has been amended as follows:

The title of the invention has been changed as follows:

-SEMICONDUCTOR DEVICE INCLUDING A LOW RESISTANCE WIRING

LAYER--.

Allowable Subject Matter

Claims 1-14 are allowed.

The following is an examiner's statement of reason for allowance:

Claims 1-6 are allowable over the prior art of record because the prior art of record does

not teach or render obvious a semiconductor device comprises a low resistance embedded wiring

layer in which a plurality of element regions are formed over the low resistance embedded

wiring layer, and in combination with all other features as recited in independent claim 1.

Claims 7-14 are allowable over the prior art of record because the prior art of record does not teach or render obvious a semiconductor device comprises a low resistance first embedded wiring layer and a low resistance second embedded wiring layer, the first embedded wiring layer is supplied with a first potential and the second embedded wiring layer is supplied with a second potential different than the first potential, and in combination with all other features as recited in independent claim 7.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance".

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Andy Huynh whose telephone number is (571) 272-1781. The examiner can normally be reached on Monday-Friday 8:30am-5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on (571) 272-1787. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 872-9306 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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04/01/05

Andy Huynh

and Music

Patent Examiner